

FIG. 1
(PRIOR ART)

FIG. 2 is a block diagram of a system 100.

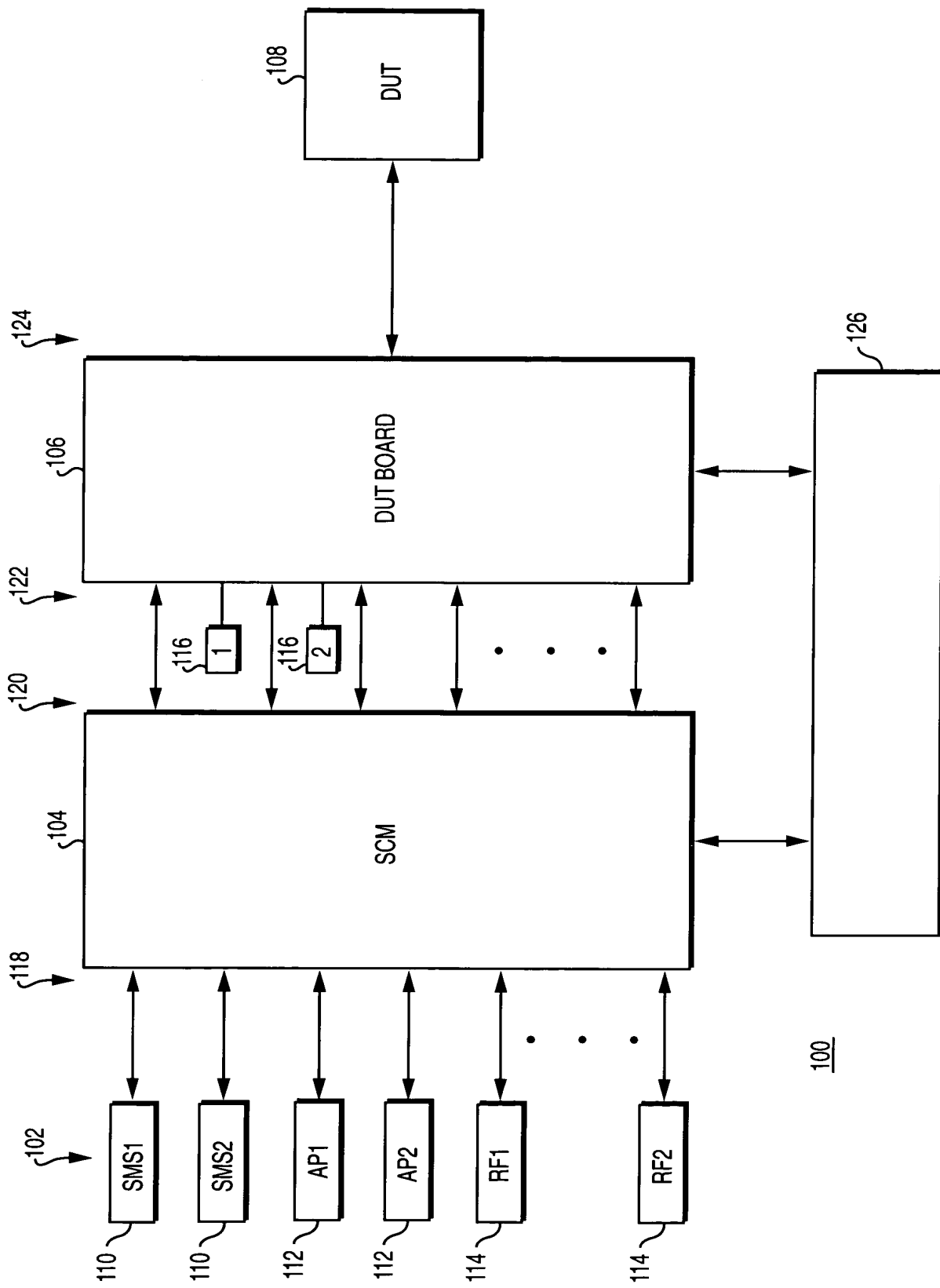


FIG. 2

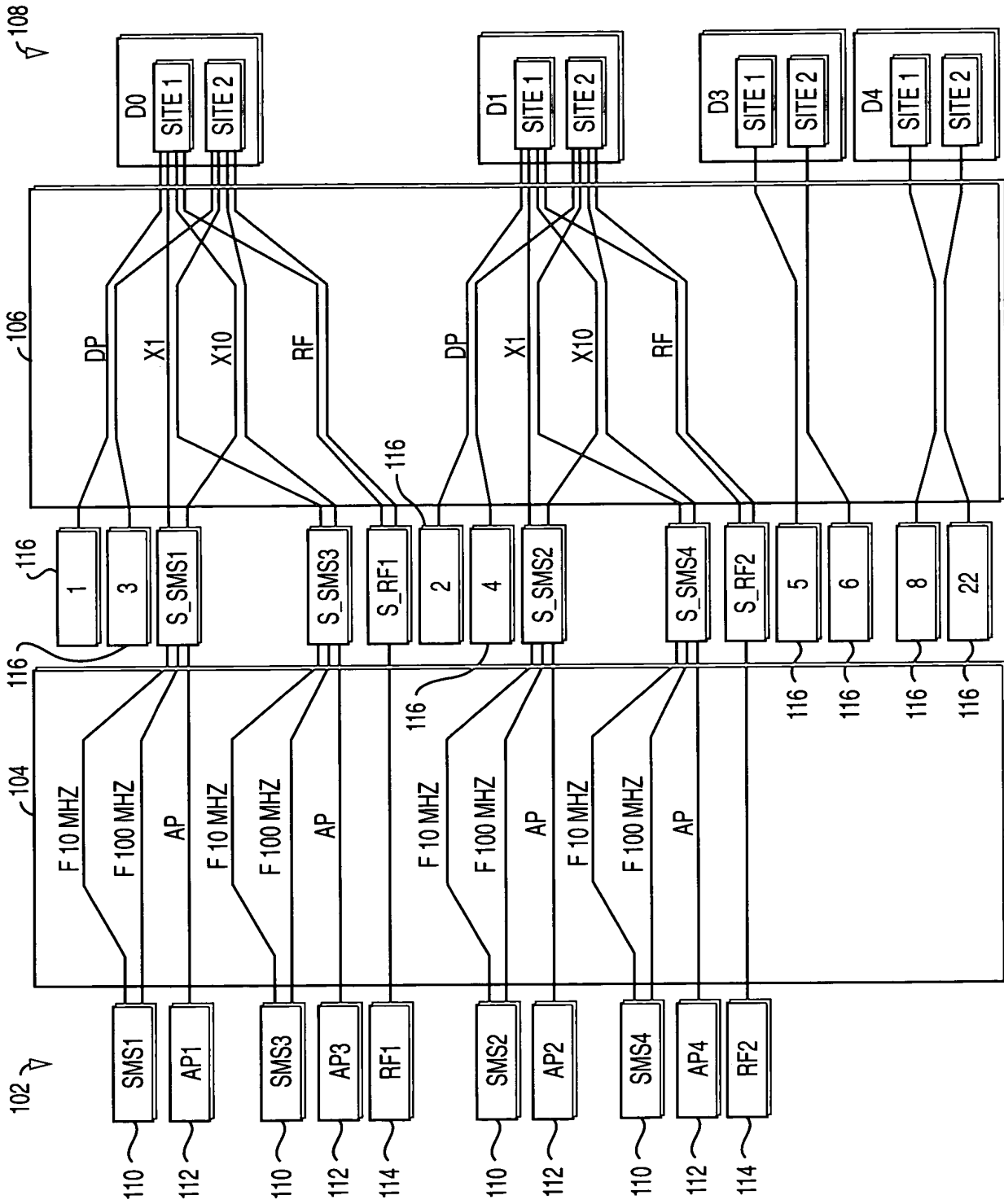


FIG. 3

TO (POGO PIN)	CONN NAME	DEF	FROM (TESTER)	CONNECT	DISCONNECT	COMMENT
S_SMS1	F_10 MHZ	X	SMS1	+K1	-K1	10 MHZ FILTER ON SMS 1
	F_100 MHZ		SMS1	+K2	-K2	100 MHZ FILTER ON SMS 1
	AP	X	AP1	+K3	-K3	AP CONNECTION TO S_SMS 1
S_SMS2	F_10 MHZ	X	SMS2	+K4	-K4	
	F_100 MHZ		SMS2	+K5	-K5	
	AP	X	AP2	+K6	-K6	
S_SMS3	F_10 MHZ	X	SMS3	+K7	-K7	
	F_100 MHZ		SMS3	+K8	-K8	
	AP	X	AP3	+K9	-K9	
S_SMS4	F_10 MHZ	X	SMS4	+K10	-K10	
	F_100 MHZ		SMS4	+K11	-K11	
	AP	X	AP4	+K12	-K12	
S_RF1			RF1			
S_RF2			RF2			

FIG. 4

TO (PIN)	PPID	X	Y	SHAPE	TYPE	CONN NAME	RES/ SITE	SITE	CON- NECT	DIS- CON- NECT	FROM (POGO PIN)	PATHS	COMMENT
D0	1	0	120	ROUND	BID	DP	PER SITE	1	+K5	-K5	1	DP, CPMU	DIRECT CONNECTION TO DP AND VIA THE CPMU
								2	+K6	-K6	3		
						X1	PER SITE	1	+K1	-K1	S_SMS1	F_10 MHZ, F_100 MHZ	DIRECT CONNECTION TO S_SMS
								2	+K2	-K2	S_SMS3		
						X10	PER SITE	1	+K7	-K7	S_SMS1	AP	X10 AMPLIFIED CONNECTION TO S_SMS
								2	+K8	-K8	S_SMS3		
						RF	RE- LAY- ED	1	+K3	-K3	S_RF1		PER SITE RELAYED CONNECTION TO RF
								2	+K4	-K4	S_RF1		
D1	2	0	240	ROUND	BID	DP	PER SITE	1	+K15	-K15	2	DP, CPMU	DIRECT CONNECTION TO DP AND VIA THE CPMU
								2	+K16	-K16	4		
						X1	PER SITE	1	+K11	-K11	S_SMS2	F_10 MHZ, F_100 MHZ	DIRECT CONNECTION TO S_SMS
								2	+K12	-K12	S_SMS4		
						X10	PER SITE	1	+K17	-K17	S_SMS2	AP	X10 AMPLIFIED CONNECTION TO S_SMS
								2	+K18	-K18	S_SMS4		
						RF	RE- LAY- ED	1	+K13	-K13	S_RF2		PER SITE RELAYED CONNECTION TO RF
								2	+K14	-K14	S_RF2		
D3	3	0	360	ROUND	BID		PER SITE	1			5	DP, CPMU	DIRECT CONNECTION TO DP AND VIA THE DP TO THE CPMU
								2			6		
D4	4	0	480	ROUND	BID		PER SITE	1			8	DP, CPMU	
								2			22		

FIG. 5

PIN NAME	FULLY QUALIFIED NAME	SCM POGO PIN		TESTER RESOURCE		COMMENT
		SITE 1	SITE 2	SITE 1	SITE 2	
ROW 1 D0	D0.DP.DP			D1	D3	D0 DEFAULTS TO THE FIRST DUT BOARD CONNECTION (DP) AND THE FIRST SCM CONNECTION (DP).
ROW 2 D0.DP	D0.DP.DP			D1	D3	D0.DP SPECIFIES THE DP DUT BOARD CONNECTION. IT THEN DEFAULTS TO THE FIRST SCM CONNECTION (DP).
ROW 3 D0.DP.DP	D0.DP.DP			D1	D3	FULLY SPECIFIED NAME
ROW 4 D0.CPMU	D0.DP.CPMU			D1	D3	CPMU CONNECTION VIA D1/D3. SEE SECTION "CPMU CONNECTION" ON PG. 5 FOR MORE DETAILS ON THE CPMU. THE DP NAME IS NOT NECESSARY SINCE THERE IS ONLY ONE CPMU SCM CONNECTION NAME.
ROW 5 D0.F_10 MHZ	D0.X1.F_10 MHZ	S_SMS1	S_SMS3	SMS1	SMS3	THE X1 DUT BOARD CONNECTION IS USED AS IT IS THE ONLY ONE TO F_10 MHZ
ROW 6 D0.X1	D0.X1.F_10 MHZ	S_SMS1	S_SMS3	SMS1	SMS3	THE X1 CONNECTION DEFAULTS TO THE FIRST SCM CONNECTION WHICH IS F_10 MHZ
ROW 7 D0.AP	D0.X10.AP	S_SMS1	S_SMS3	AP1	AP3	THE ONLY AP CONNECTION IS VIA X10
ROW 8 D0.X10	D0.X10.AP	S_SMS1	S_SMS3	AP1	AP3	THE ONLY X10 CONNECTION IS AP
ROW 9 D0.RF	D0.RF	S_RF1	S_RF1	RF1	RF1	

FIG. 6